KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
(An Autonomous Institute under Kakatiya University, Warangal)
Opp : Yerragattugutta, Bheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA.
काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना राज्य, भारत
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015. తెలంగాణ రాష్ట్రం. రాజదేశము
Office of the Controller of Examinations

No. MSE-I Schedule/ B.Tech V-Sem ((URR-18R22)/2025/710-2

Date: 30-08-2025

MID SEMESTER EXAMINATIONS-I (MSE-I) FOR B.TECH. V-SEMESTER (URR-18R22), SEPTEMBER-2025

TIME - TABLE

Examination Timings: FN   10:00 am to 12:00 pm

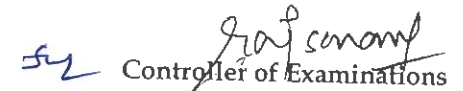| Date & Day of Examination	CE	ME	CSE	EEE	ECE	IT	CSE(N)	ECI	CSE (IoT)	CSE (AIML)	CSE (DS)
08.09.2025 Monday	Universal Human Values –II	* Quantitative Aptitude & Logical Reasoning	* Quantitative Aptitude & Logical Reasoning	Universal Human Values –II	Universal Human Values – II	* Quantitative Aptitude and Logical Reasoning	* Quantitative Aptitude and Logical Reasoning	Universal Human Values – II	* Quantitative Aptitude & Logical Reasoning	Universal Human Values –II	Universal Human Values – II
09.09.2025 Tuesday	PE-I Advanced Concrete Technology	Object Oriented Programming through JAVA	PE-I Artificial Intelligence	PE-I Renewable Energy Systems	PE-I Artificial Intelligence and Machine Learning with Python	Design and Analysis of Algorithms	PE-I Artificial Intelligence	PE-I Internet of Things	PE-I Operating System	Internet of Things	Design and Analysis of Algorithms
10.09.2025 Wednesday	Structural Analysis	Dynamics of Machinery	Software Engineering	Power Systems-II	Communication Systems	PE-I Principles of Programming Languages	Internet of Things	Analog and Digital Communications	Software Engineering	Software Engineering	Software Engineering
11.09.2025 Thursday	Environmental Engineering	Machine Tools and Metrology	Compiler Design	Microprocessors and Microcontroller Systems	Microprocessors and Microcontrollers	Compilers	Compiler Design	Microcontrollers and Embedded Systems	Compiler Design	Compiler Design	Compiler Design
12.09.2025 Friday	Soil Mechanics	PE-I Design of Transmission Systems	Computer Networks	Power Electronics	Linear Integrated Circuits and Applications	Computer Networks	Advanced Computer Networks	Linear Integrated Circuits and Applications	IoT with Cloud Computing	PE-I Practical approach to AI and Introduction to Quantum Computing	PE-I Computer Networks
13.09.2025 Saturday	Design of Steel Structures	IC Engines and Gas Turbines	Python Programming	Electrical Machines-II	Linear Control Systems	Web Technologies	Machine Learning	Linear Control Systems	Database Management Systems	Machine Learning	Data Mining and Data Warehousing
15.09.2025 Monday	--	--	--	--	Antennas and Wave Propagation	--	--	--	--	--	--

To: Exam Branch Notice Boards

Candidate should occupy his/her seat at least <u>10 minutes before</u> the commencement of the examination.

Copy to: 1. Principal *for favour of information* 2. Dean, Academic Affairs
3. HOD's - with a request to display in the Dept Notice Boards & share in the Faculty & Class WhatsApp groups
4. Training & Placement Officer   5. I/c Web Team - with a request to display on the www.kitsw.ac.in/examinations/Examinations_schedules.html

Controller of Examinations

* Note: QALR Examination shall be conducted in ONLINE mode through the NeoPAT Portal